

HCMOS-Logic
Digital Delay Units

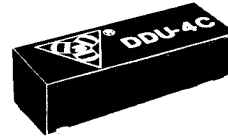
SERIES: DDU-4C

**5 Outputs
 14 pins DIP**



Features:

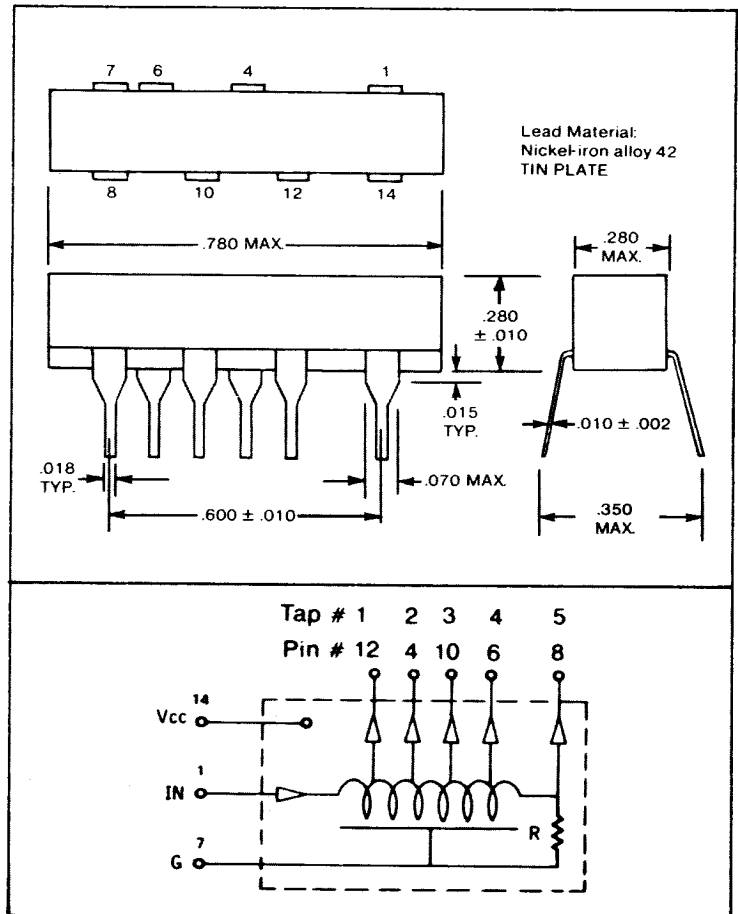
- Completely interfaced for CMOS.
- No external components required.
- P.C. board space economy achieved.
- Low profile.
- Fits standard 14 pins DIP socket.



Specifications:

- No. Taps: 5 equally spaced taps.
- Total delay tolerance: 5% or 2 NS whichever is greater.
- Rise-time: 8 NS typ.
- Temperature coefficient: 300 PPM/° C.
- Temperature range: 0° C to + 70° C.
- Supply voltage: 5 Vdc ± 5%.
- Logic 1 input: 3.2V.
- Logic 0 input: .9V.
- Logic 1 output: 4.5V.
- Logic 0 output: 0.1V.
- Input current: 1 µa max.
- Supply current:
 - I_{CC}H = 10 MA
 - I_{CC}L = 40 µa
- Fan-out: 10 LSTTL loads min.

Part No.	Total Delay (NS)	Tap Delay (NS)
DDU-4C-5050	50	10 ± 2
DDU-4C-5060	60	12 ± 2
DDU-4C-5075	75	15 ± 2
DDU-4C-5100	100	20 ± 2
DDU-4C-5125	125	25 ± 2
DDU-4C-5150	150	30 ± 3
DDU-4C-5175	175	35 ± 3
DDU-4C-5200	200	40 ± 4
DDU-4C-5250	250	50 ± 5



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